

Substitute for form 1449B/PTO

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(use as many sheets as necessary)

Sheet 1 of 1

Complete if Known

Application Number	10/723,592
Filing Date	November 26, 2003
First Named Inventor	Augustus K. Uht
Art Unit	2631
Examiner Name	Phuong M. Phu
Attorney Docket Number	022193-010111US

NON PATENT LITERATURE DOCUMENTS

Examiner Initials *	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
PP	1	"AMD Power Now™ Technology," product information Advanced Micro Device Sunnyvale, CA USA (2002).	
PP	2	"Mobile Intel® Pentium® III Processors Intel SpeedStep® Technology," product information available from http://www.intel.com , Intel Corporation Santa Clara, CA USA (2004).	
PP	3	AUSTIN et al. "Making Typical Silicon Matter with Razor", IEEE Computer pp. 57-65 March 2004 (2004).	
PP	4	BURD et al. "A Dynamic Voltage Scaled Microprocessor System," IEEE Journal of Solid-State Circuits 35:1571-1580 (2000).	
PP	5	GINOSAR "Adaptive Synchronization" proceedings of the IEEE International Conference on Computer Design (ICCD), Oct. 1998 (1988).	
PP	6	KURODA et al "Variable Supply-Voltage Scheme for Low-Power High-Speed CMOS Digital Design," IEEE Journal of Solid-State Circuits 33:454-462 (1998).	
PP	7	MERCHANT et al. "Analysis of a control mechanism for a variable speed processor," IEEE Transactions on Computers 45:793-801 (1996).	
PP	8	OLIVIERI "A Low-Power Microcontroller with on-Chip Self-Tuning Digital Clock-Generator for Variable-Load Applications," proceedings of the IEEE International Conference on Computer Design October 10 - 13, 1999 Austin, Texas (1999).	
PP	9	SJOGREN et al. "Interfacing synchronous and asynchronous modules within a high-speed pipeline," IEEE Transactions on VLSI Systems 8:573-583 (2000).	
PP	10	Suzuki "Low Power Adder with Adaptive Supply Voltage," IEEE 21st International Conference on Computer Design October 13 - 15, 2003 San Jose, California (2003).	
PP	11	TZARTZANIS et al. "A 34Word x 64b 10R/6W Write-Through Self-Timed Dual-Supply Voltage Register File," Proceedings of the 2002 IEEE International Solid State Circuits Conference (2002).	

Examiner Signature	Phuong Phu	Date Considered	12/14/04
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* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Applicant's unique citation designation number (optional). ² Applicant is to place a check mark here if English language Translation is attached.